

Fig. 2a PRIOR ART

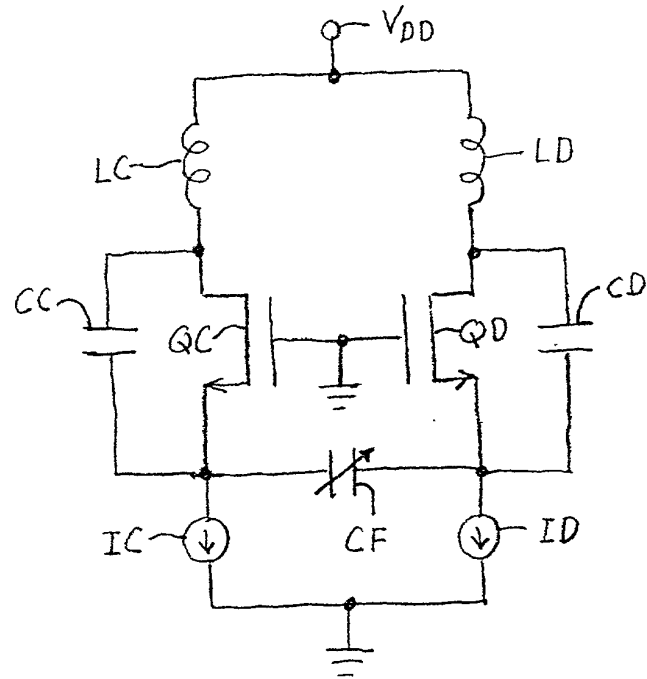


Fig. 2b PRIOR ART

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Fig. 3
PRIOR ART

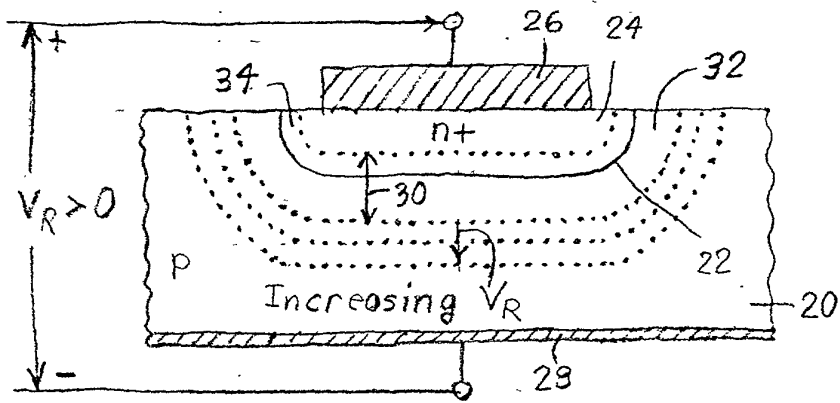


Fig. 4
PRIOR ART

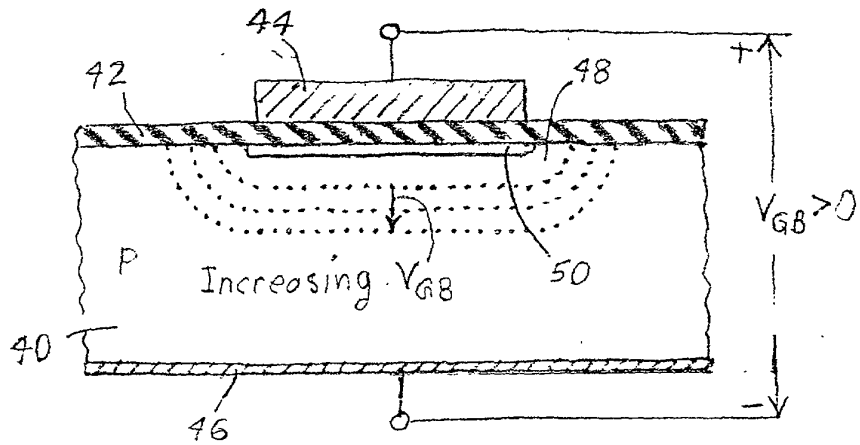
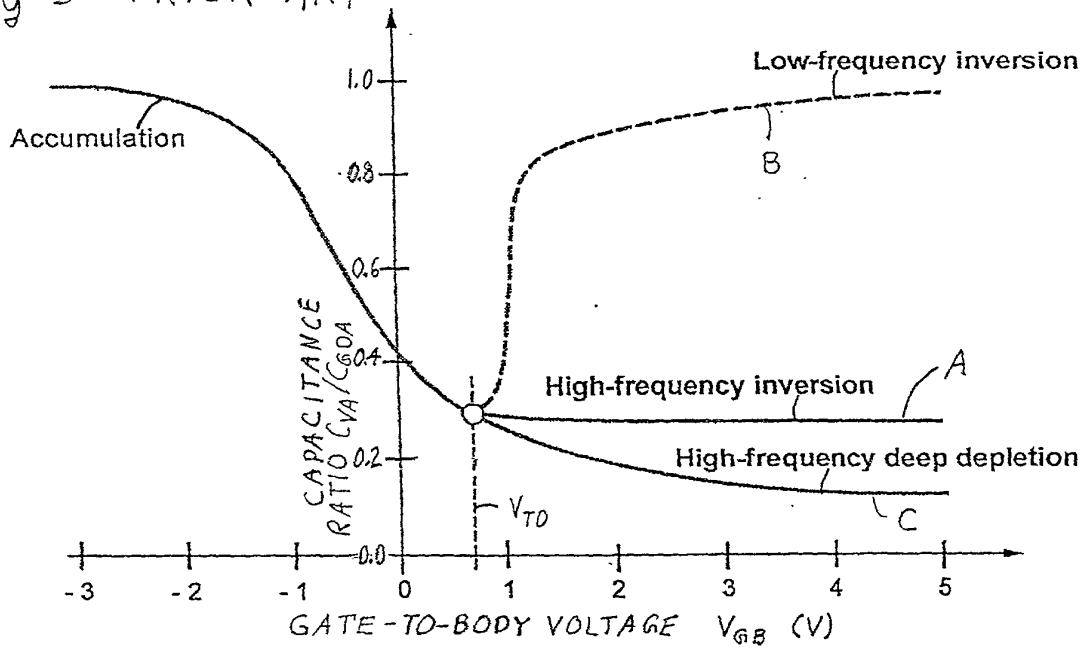


Fig 5 PRIOR ART



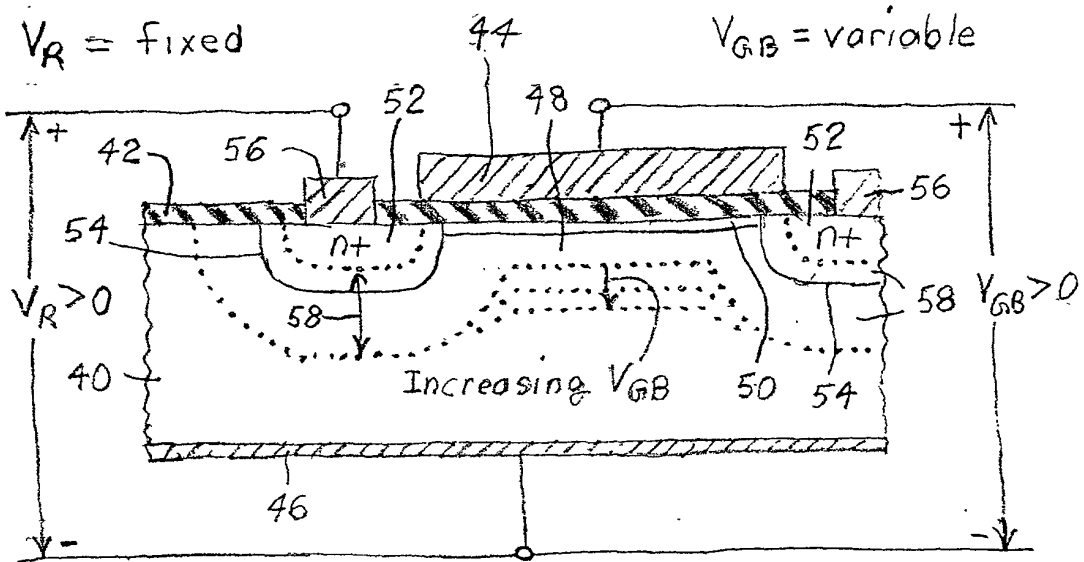


Fig. 6 PRIOR ART

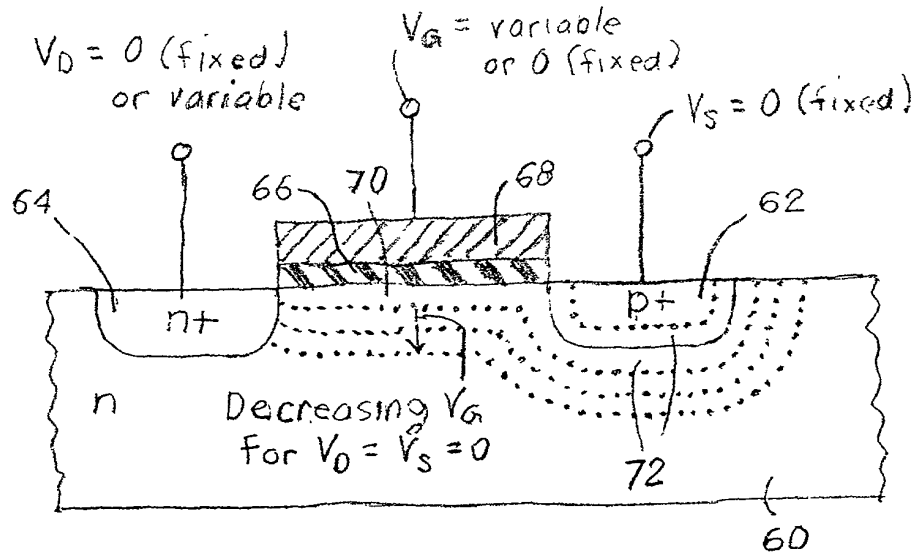


Fig. 7 PRIOR ART

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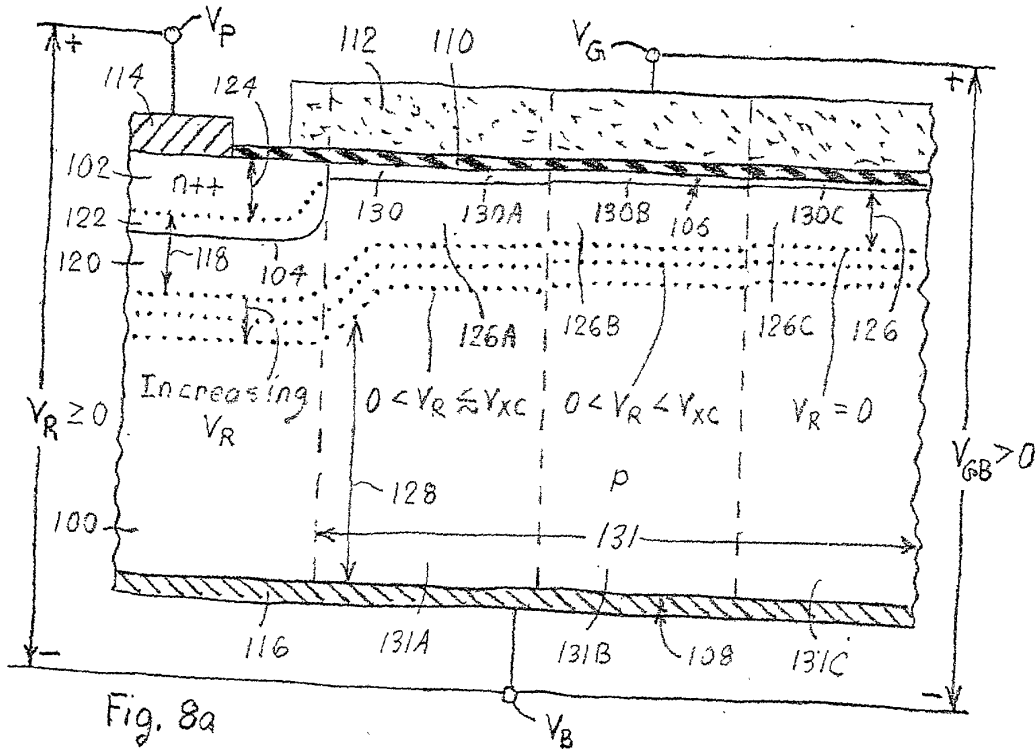


Fig. 8a

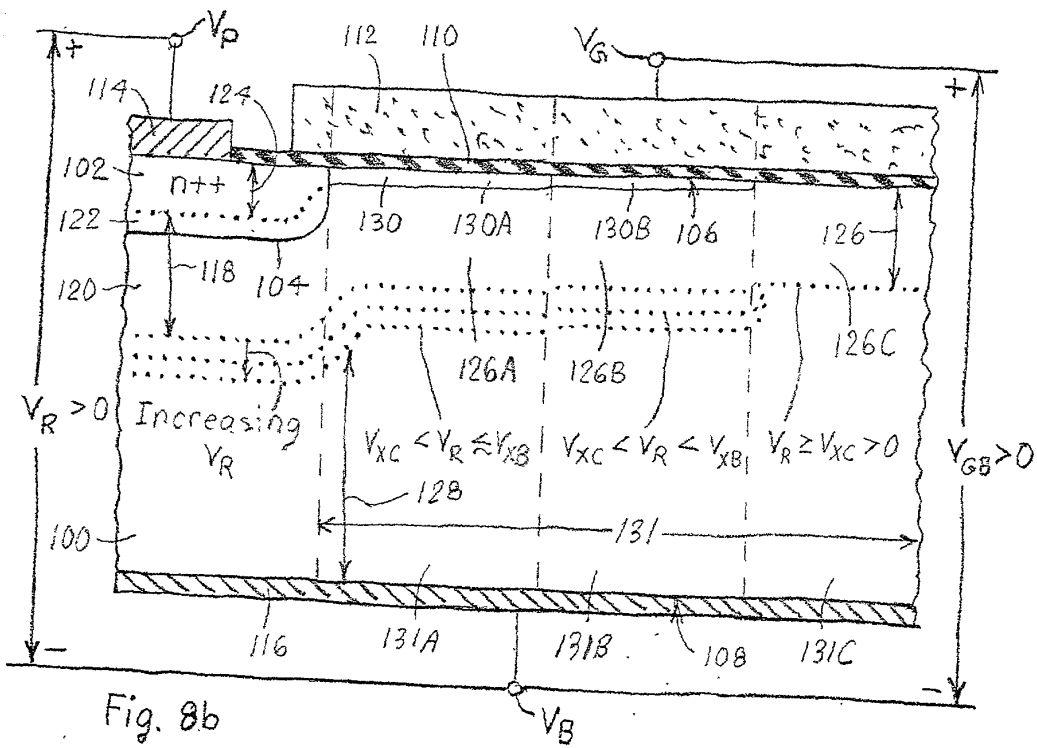


Fig. 8b

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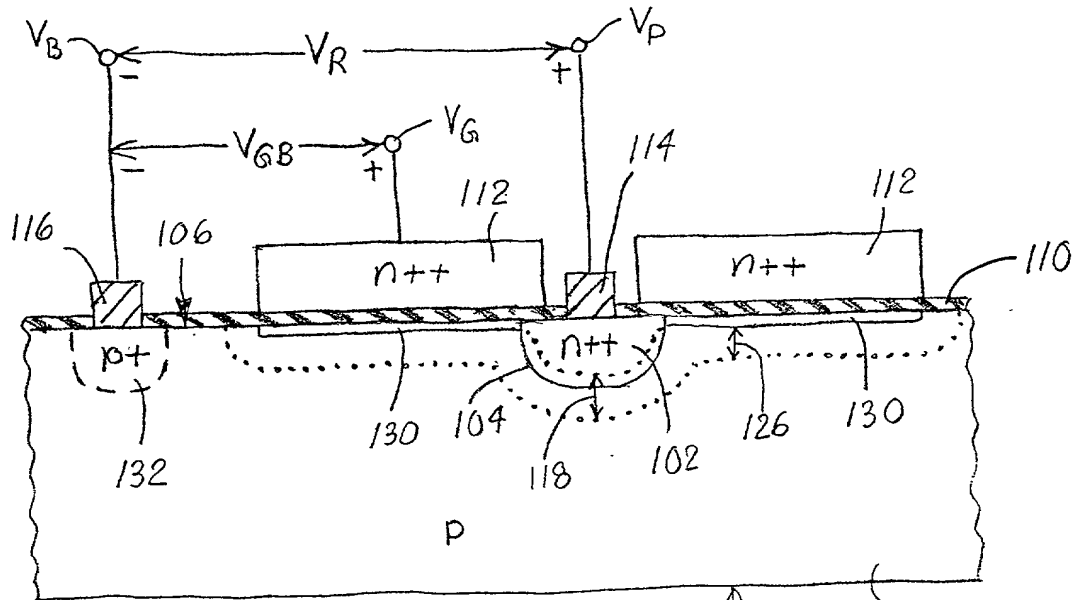


Fig. 9a

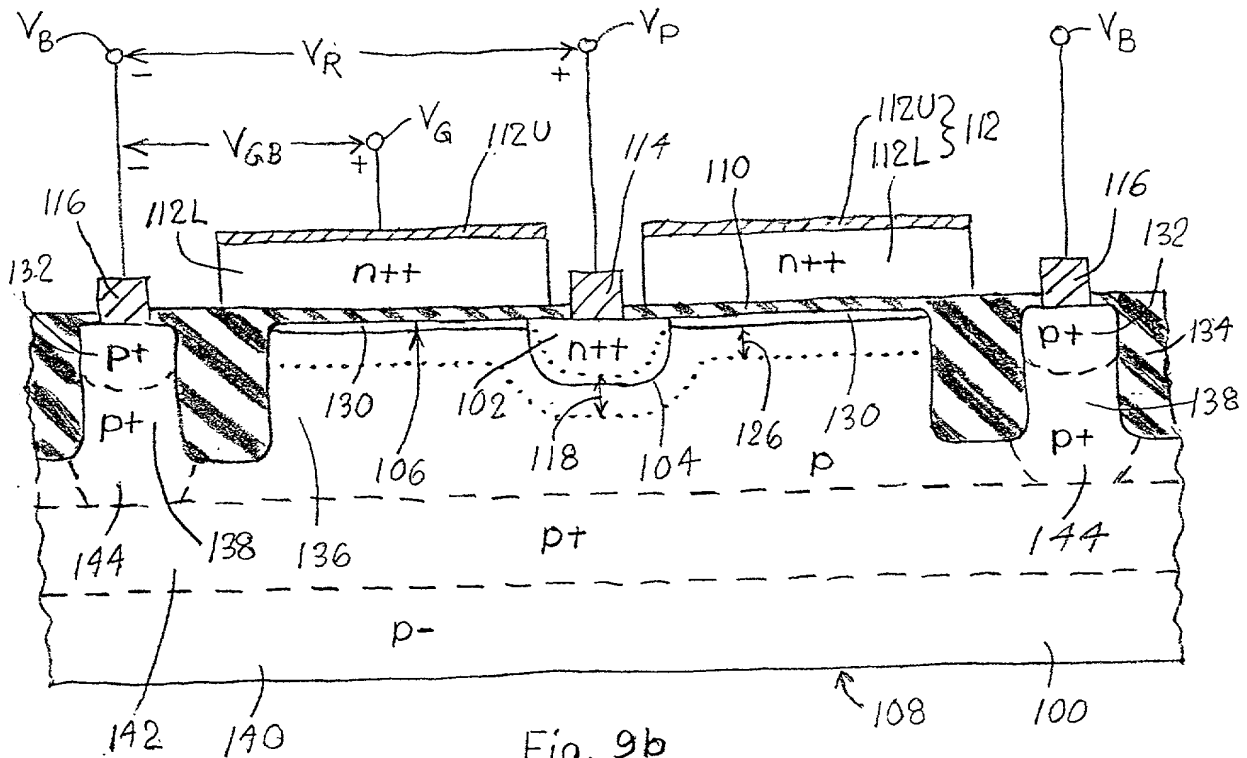
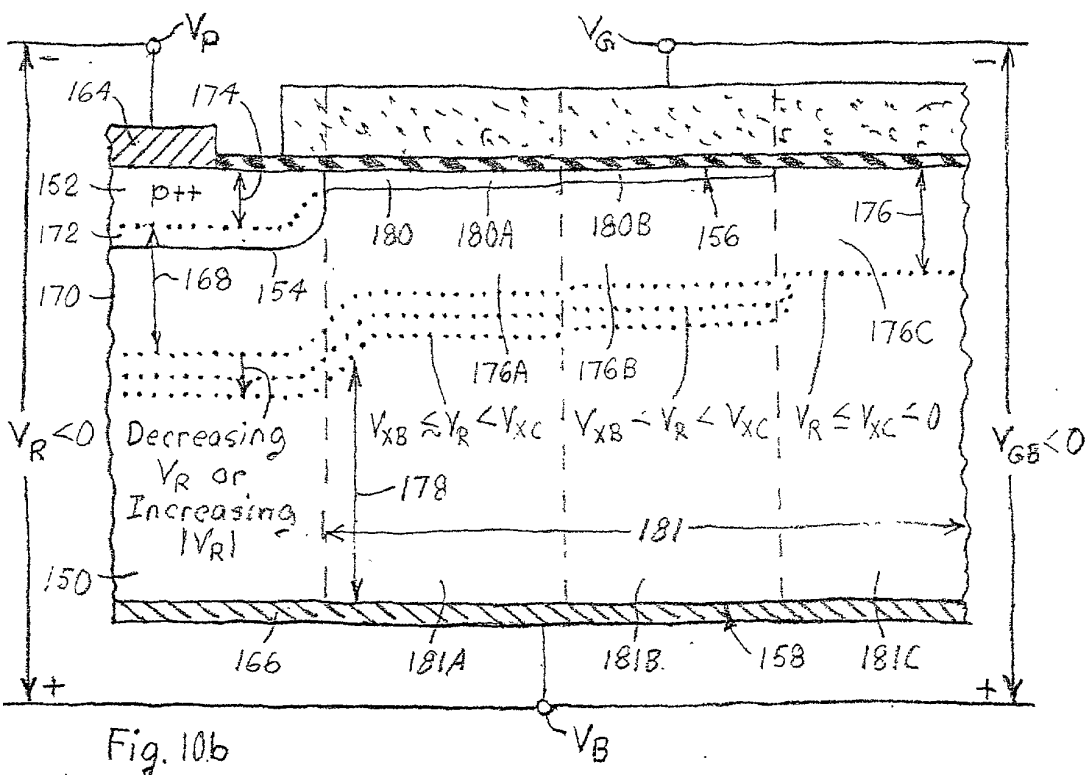
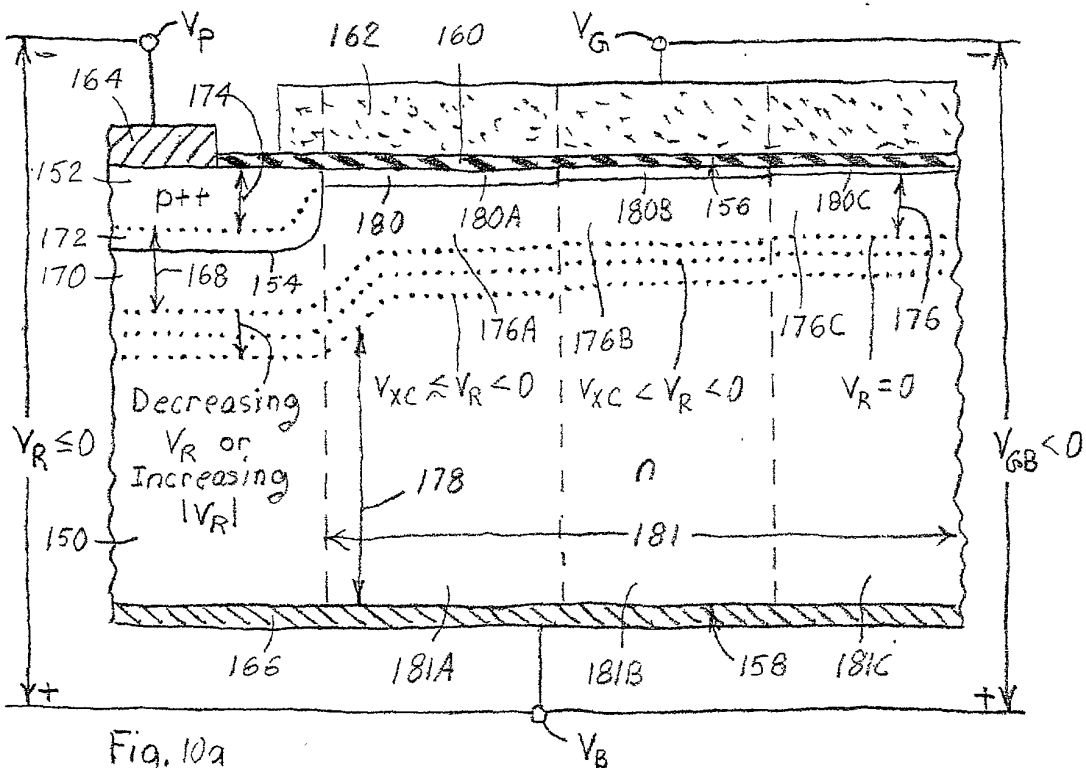
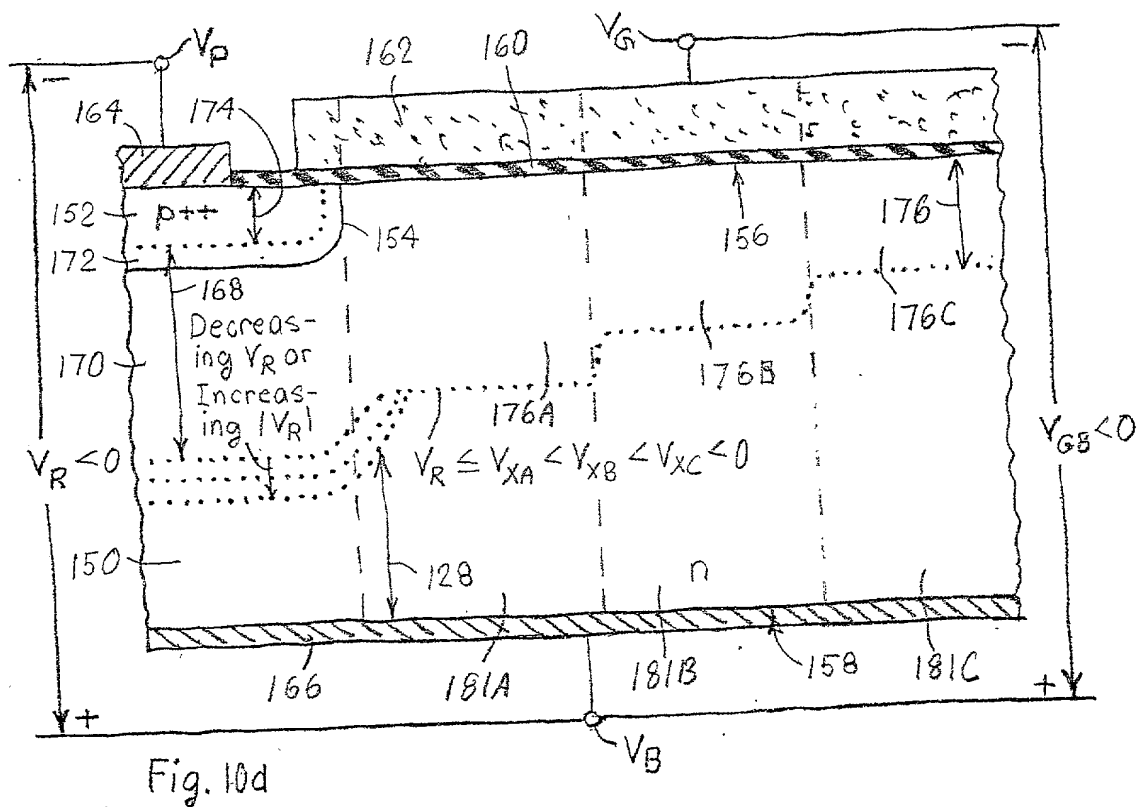
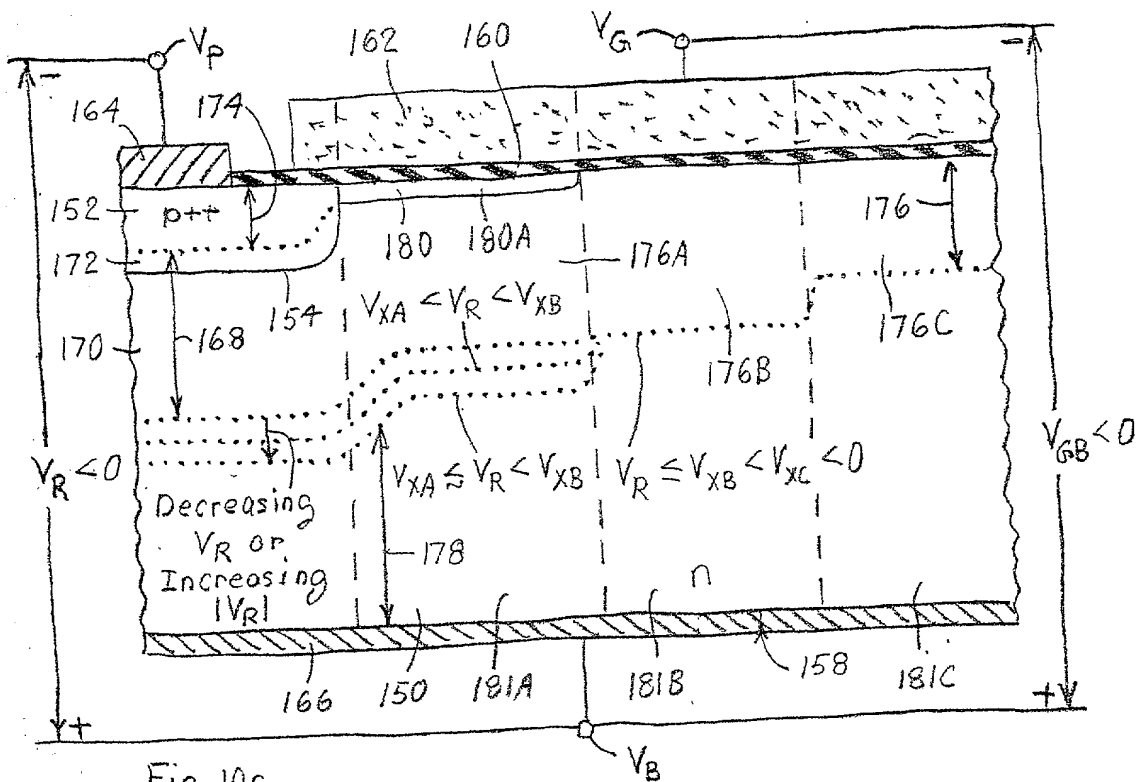


Fig. 9b



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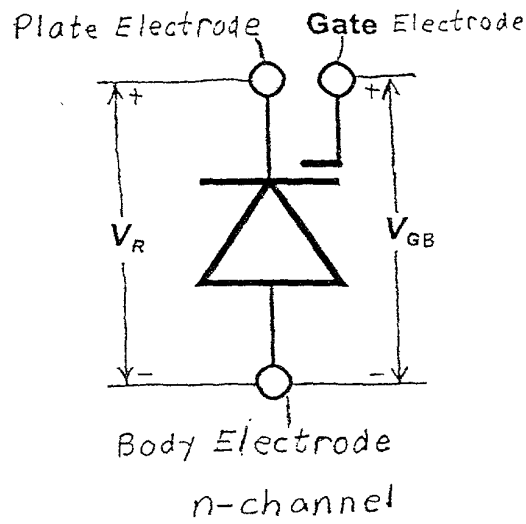


Fig. 11a

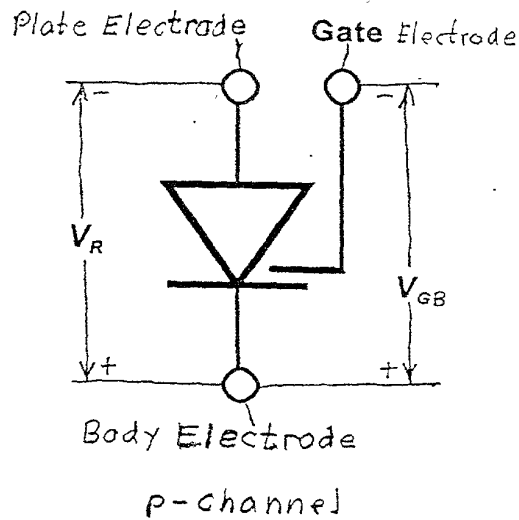
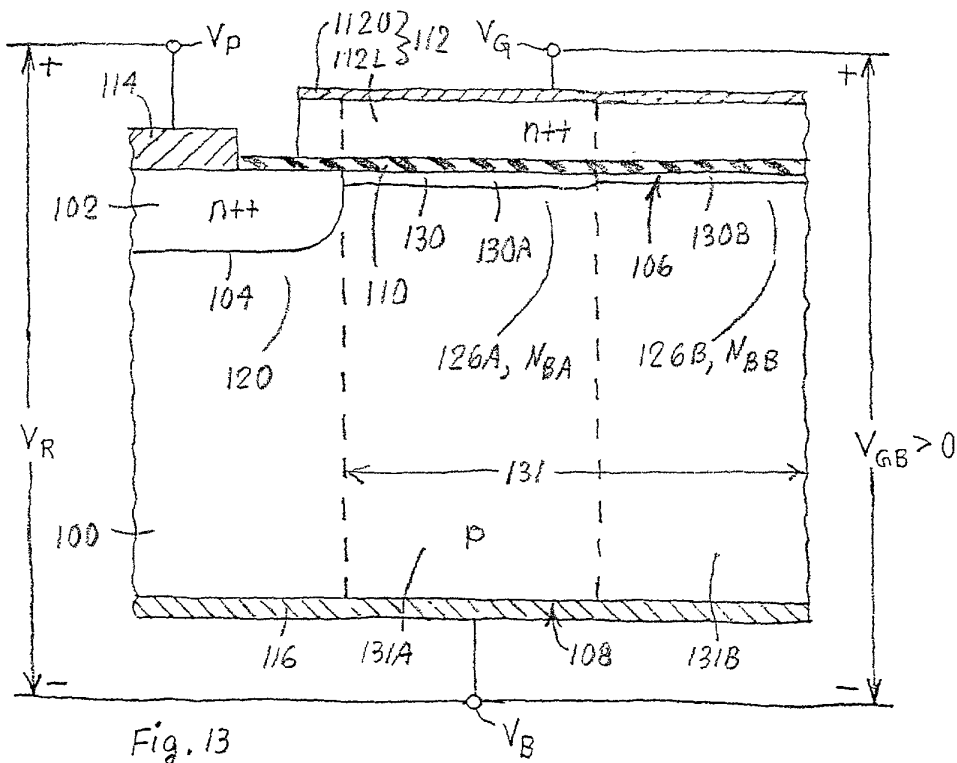
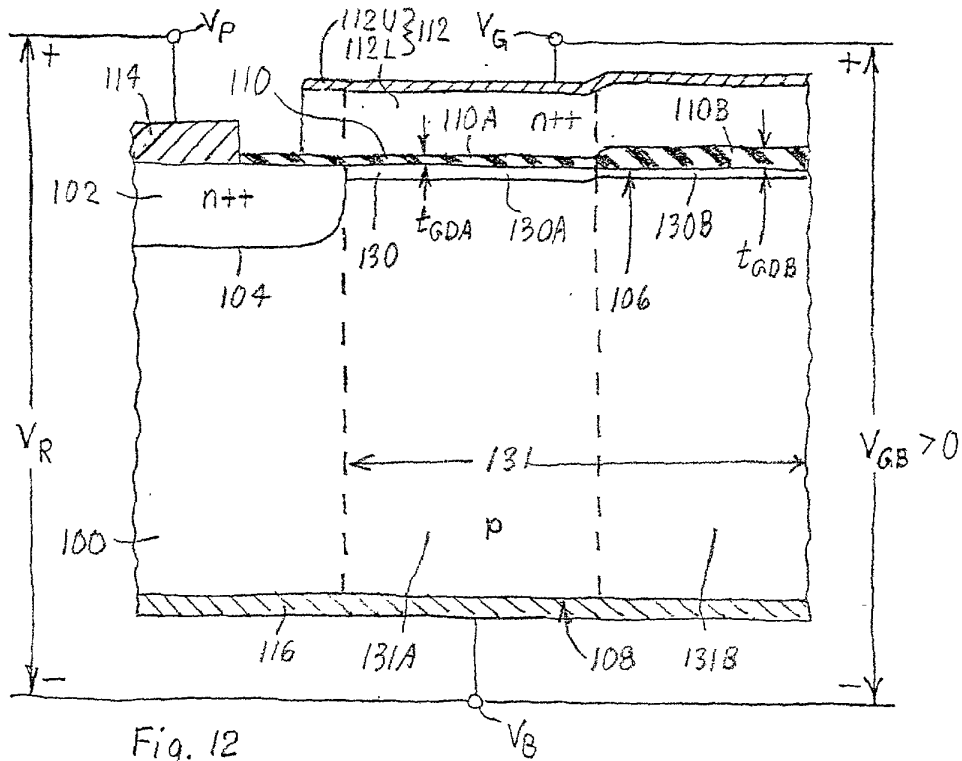


Fig. 11b

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NS-5127

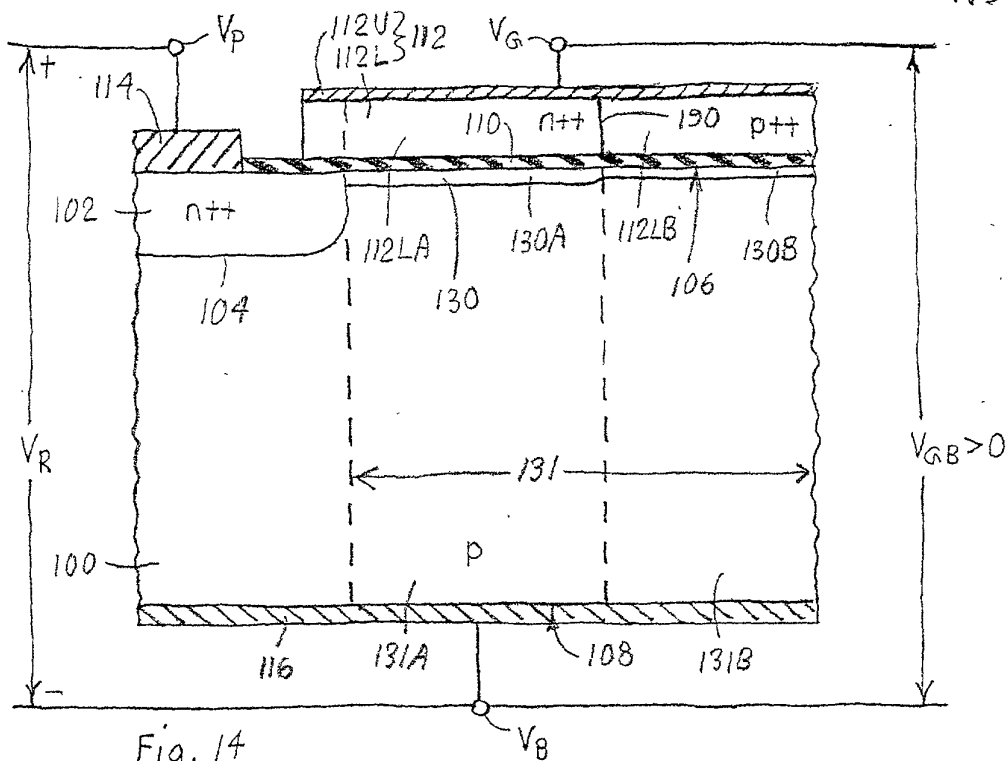


Fig. 14

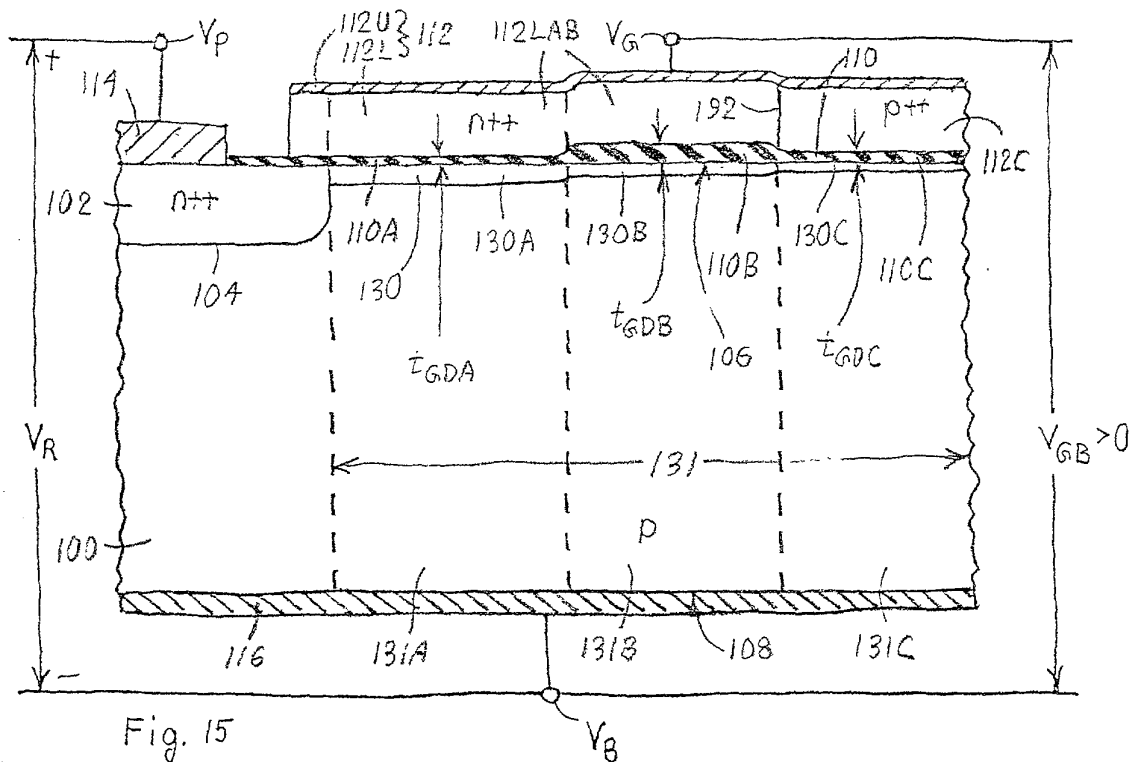
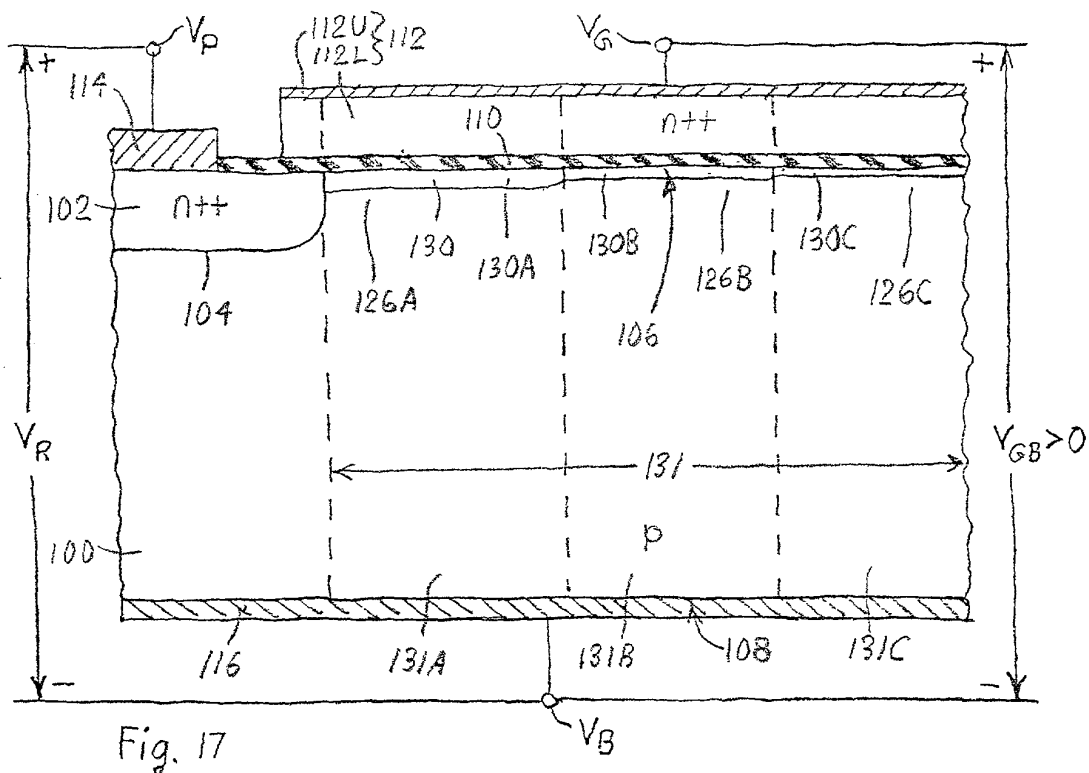
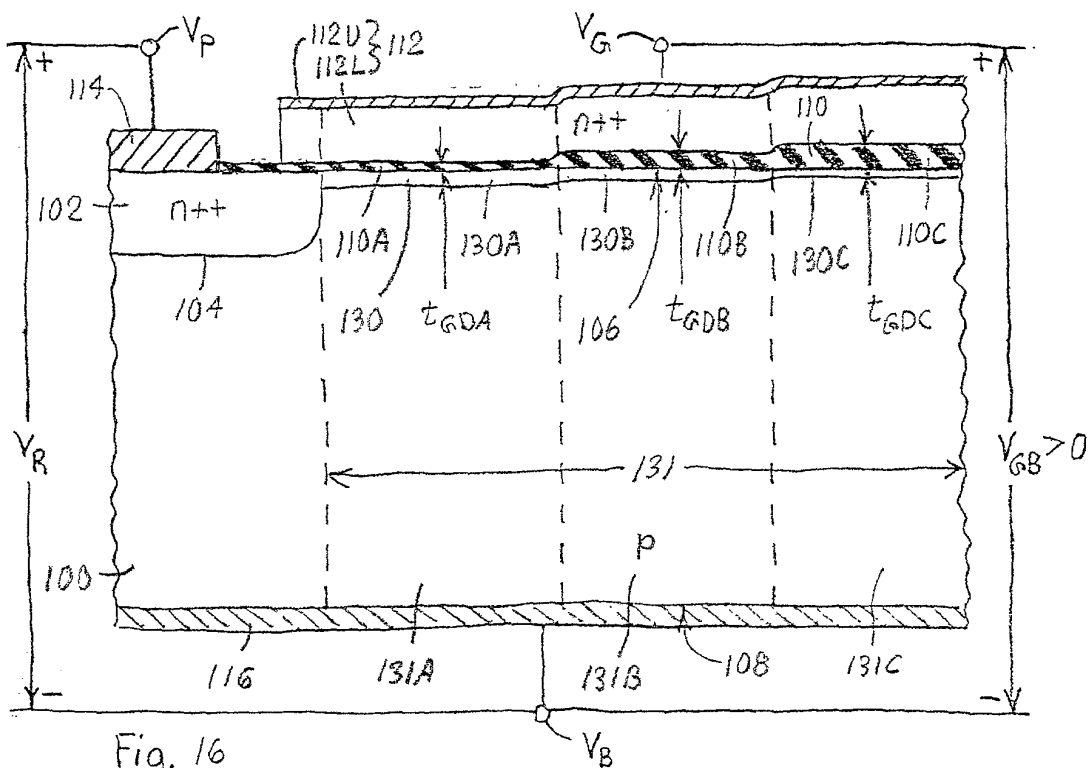


Fig. 15

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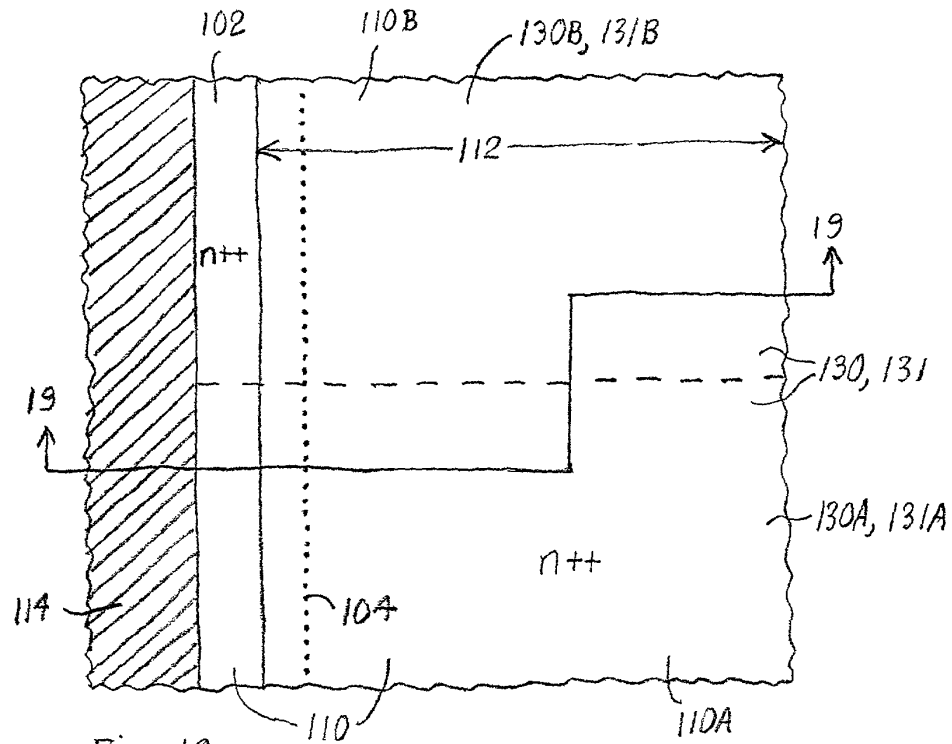


Fig. 18

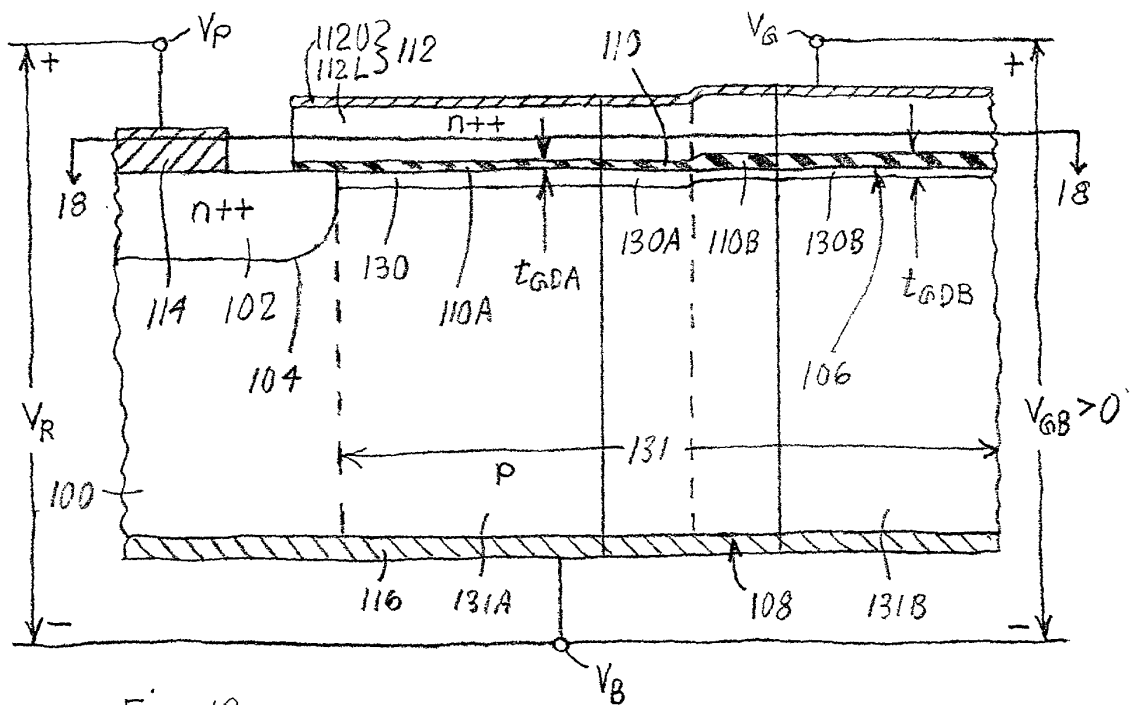


Fig. 19

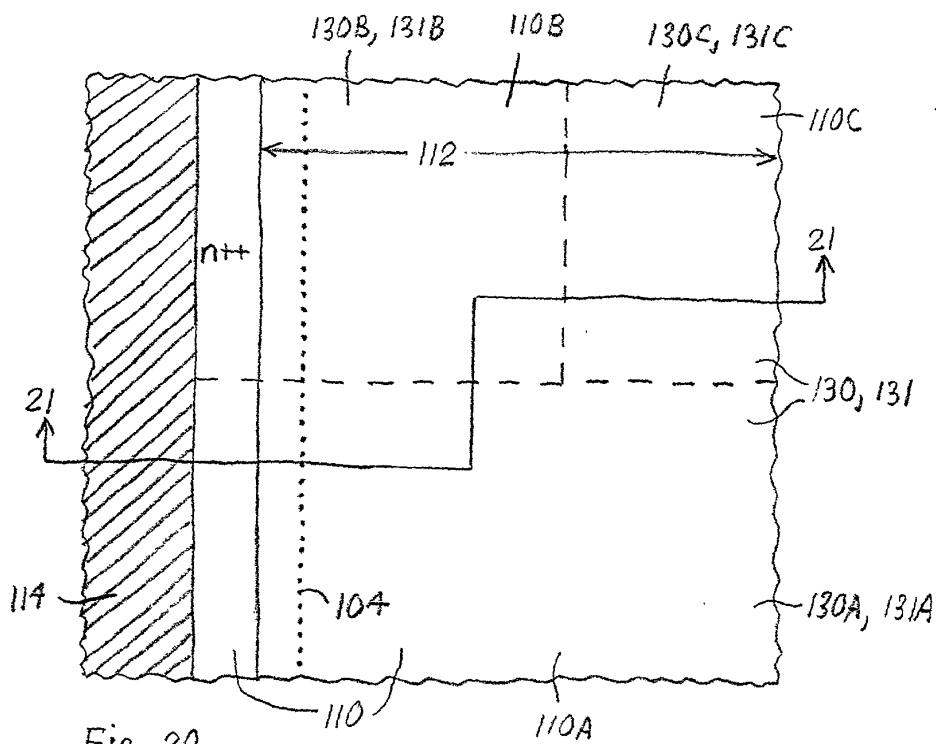


Fig. 20

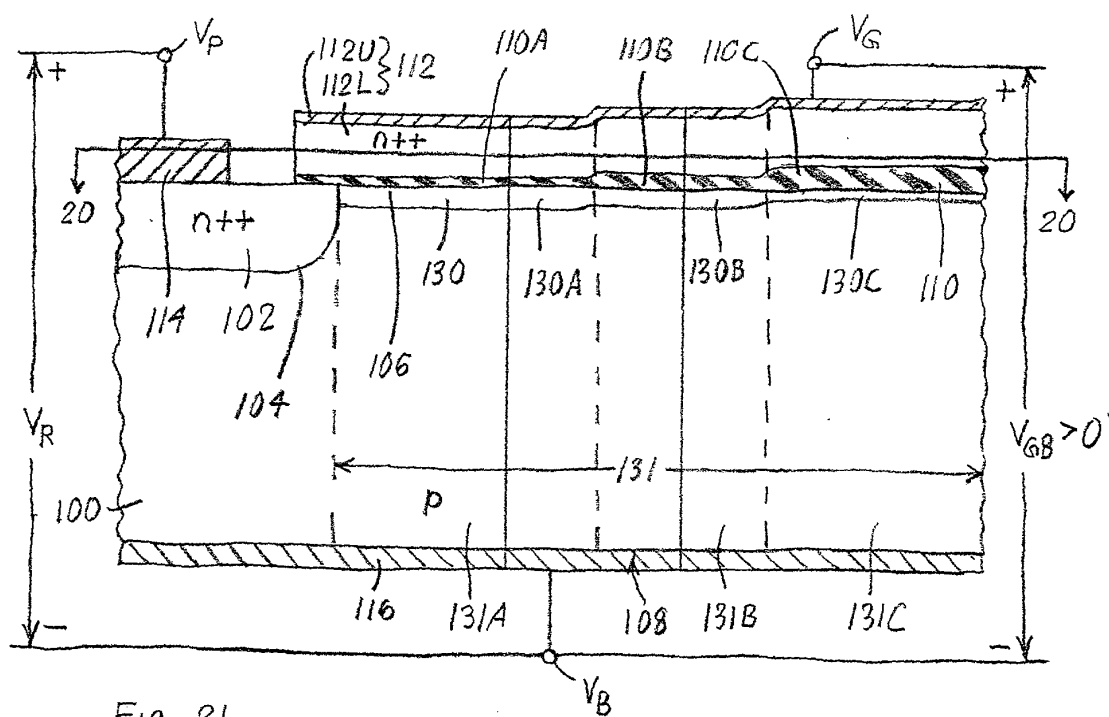


Fig. 21

Fig. 22

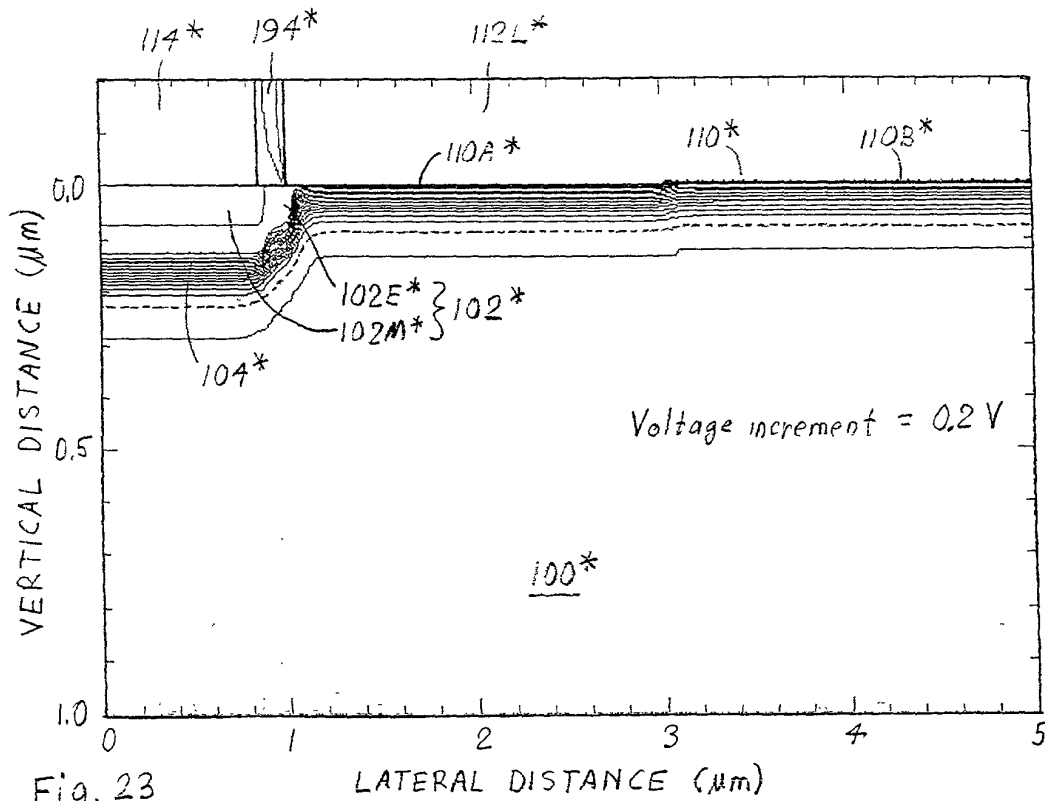
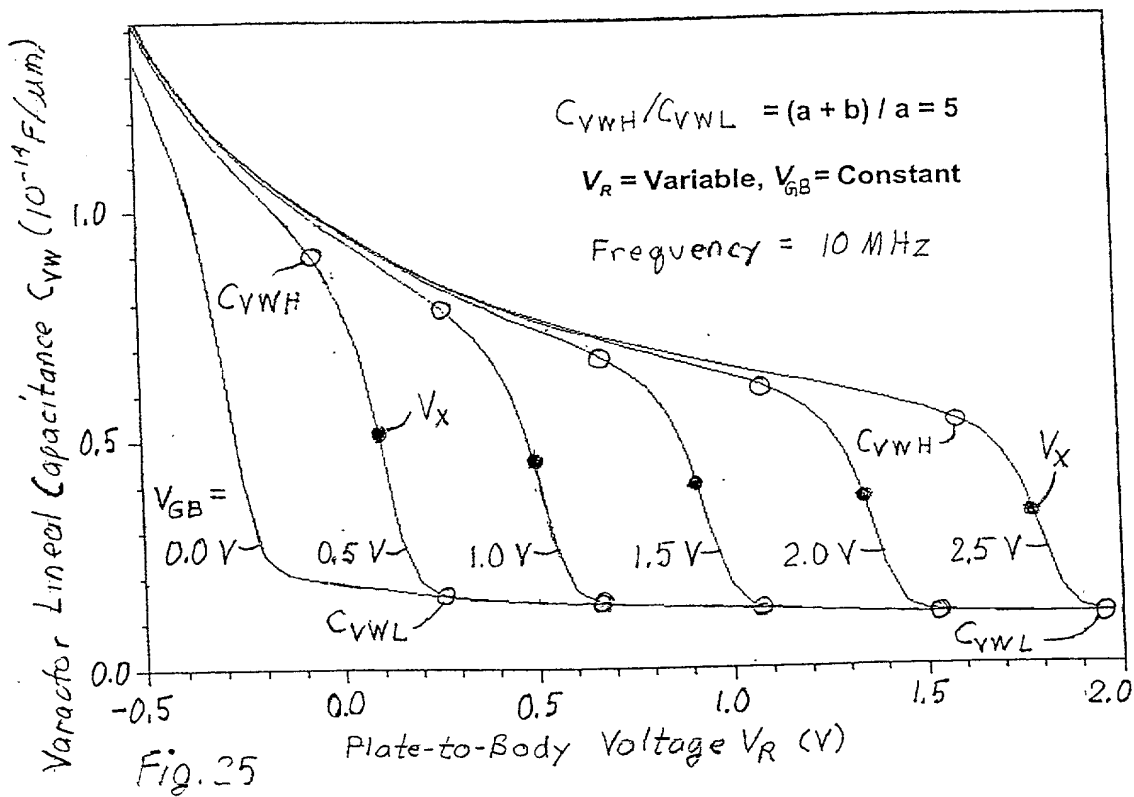
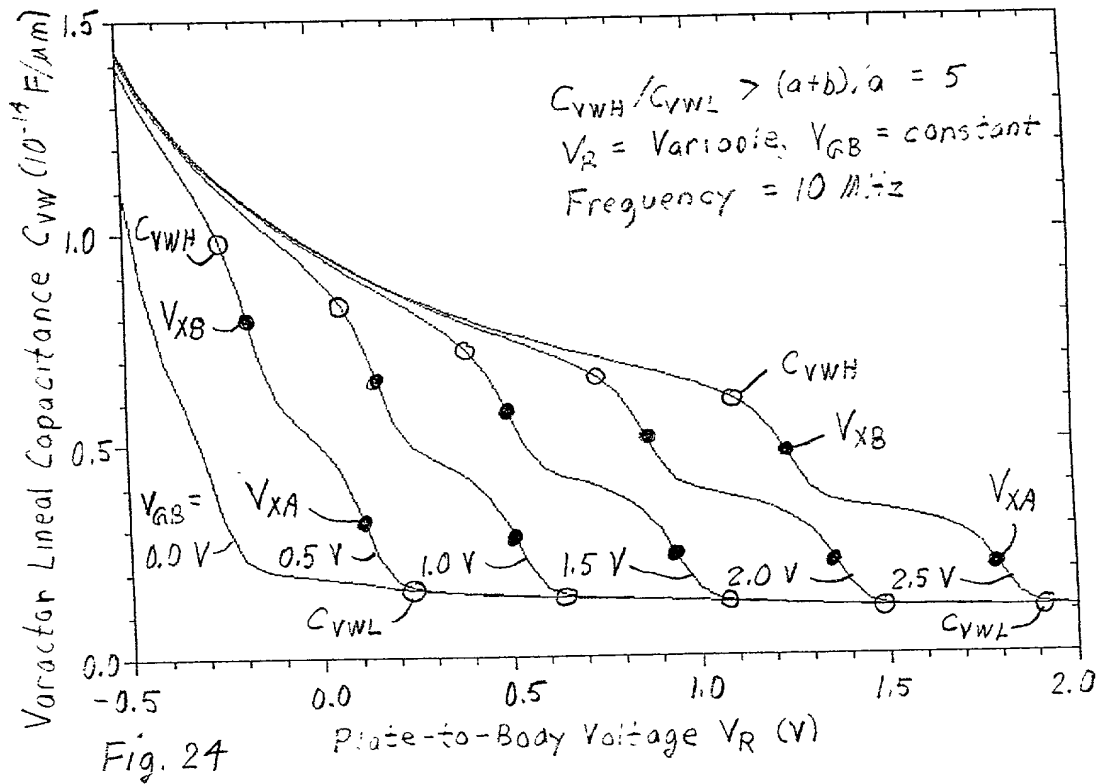
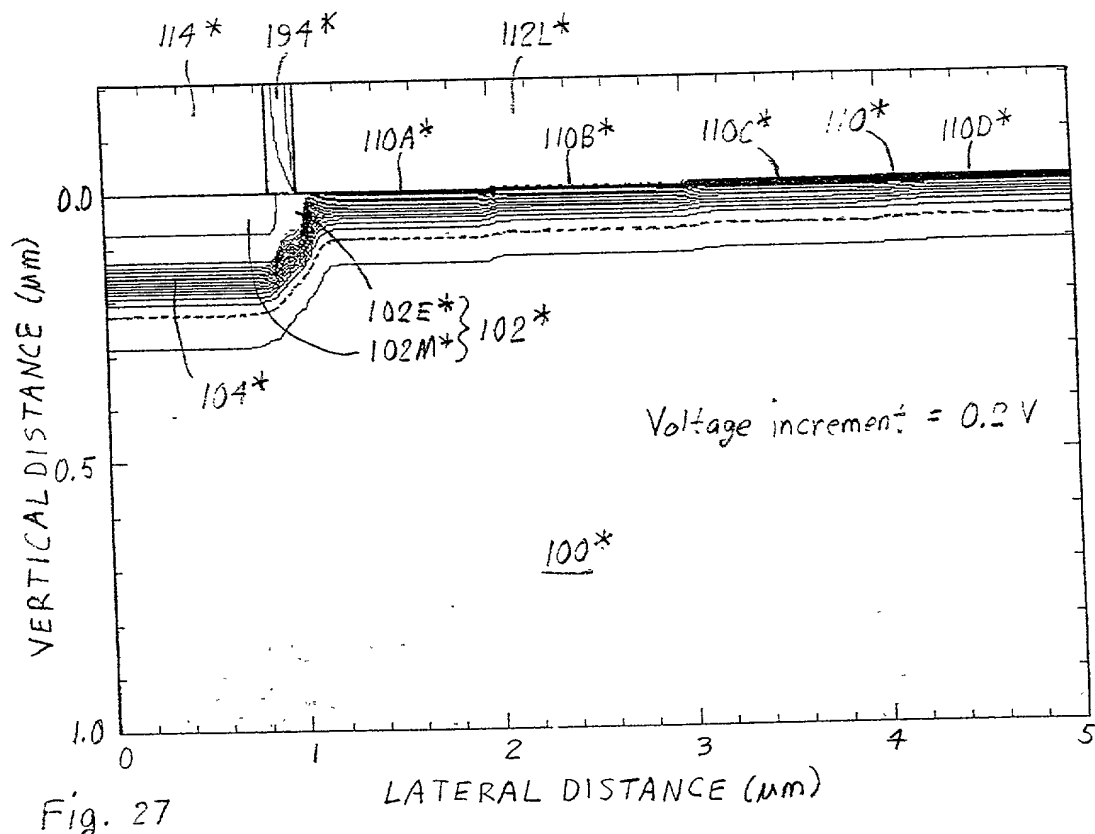
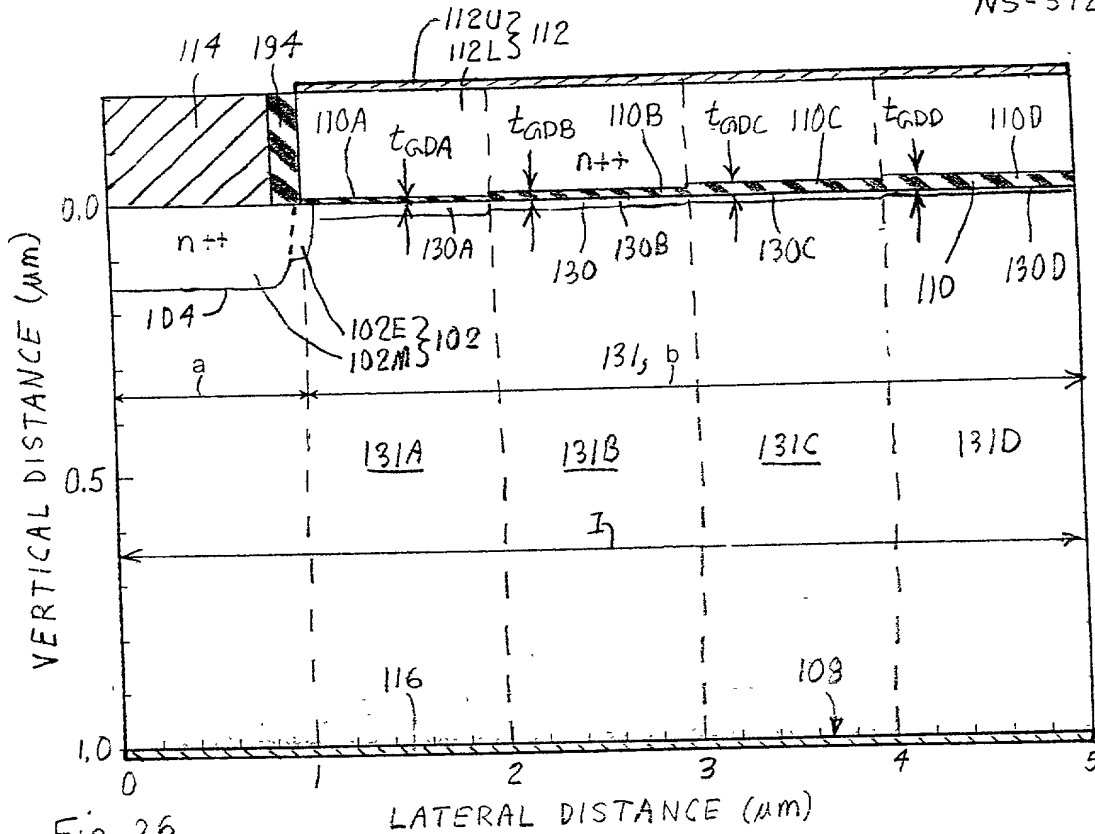


Fig. 23

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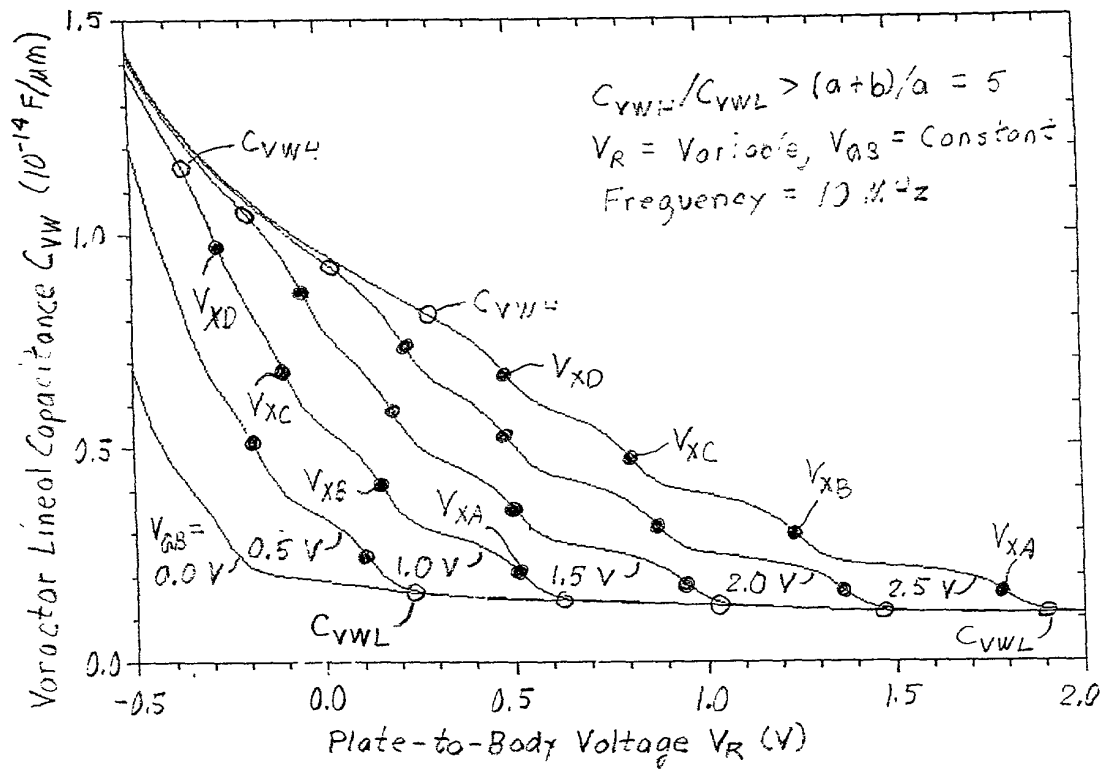


Fig. 28

Fig. 29

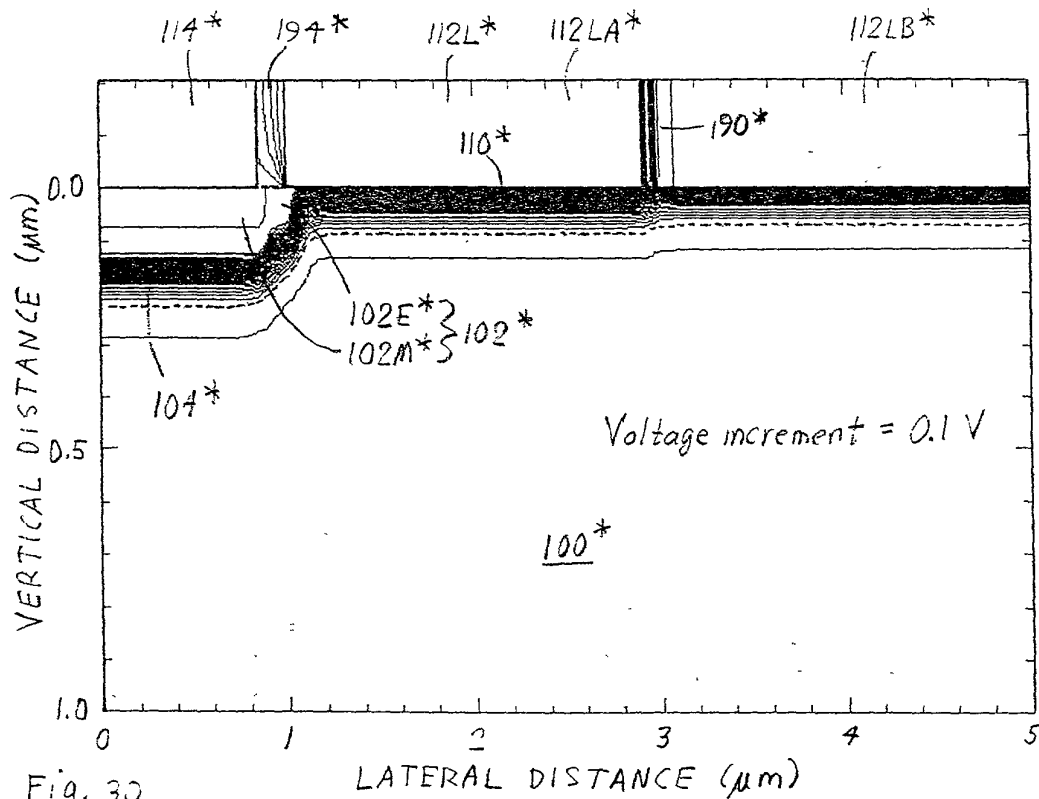


Fig. 30

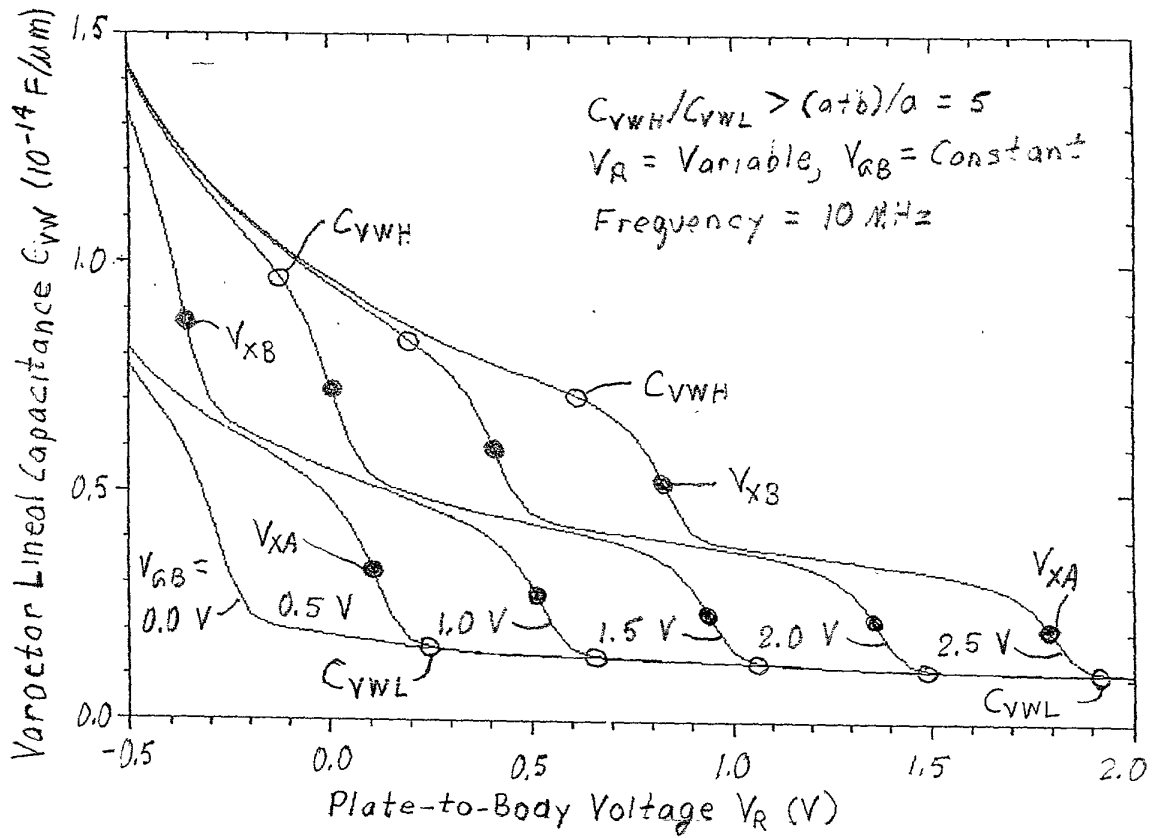


Fig. 31

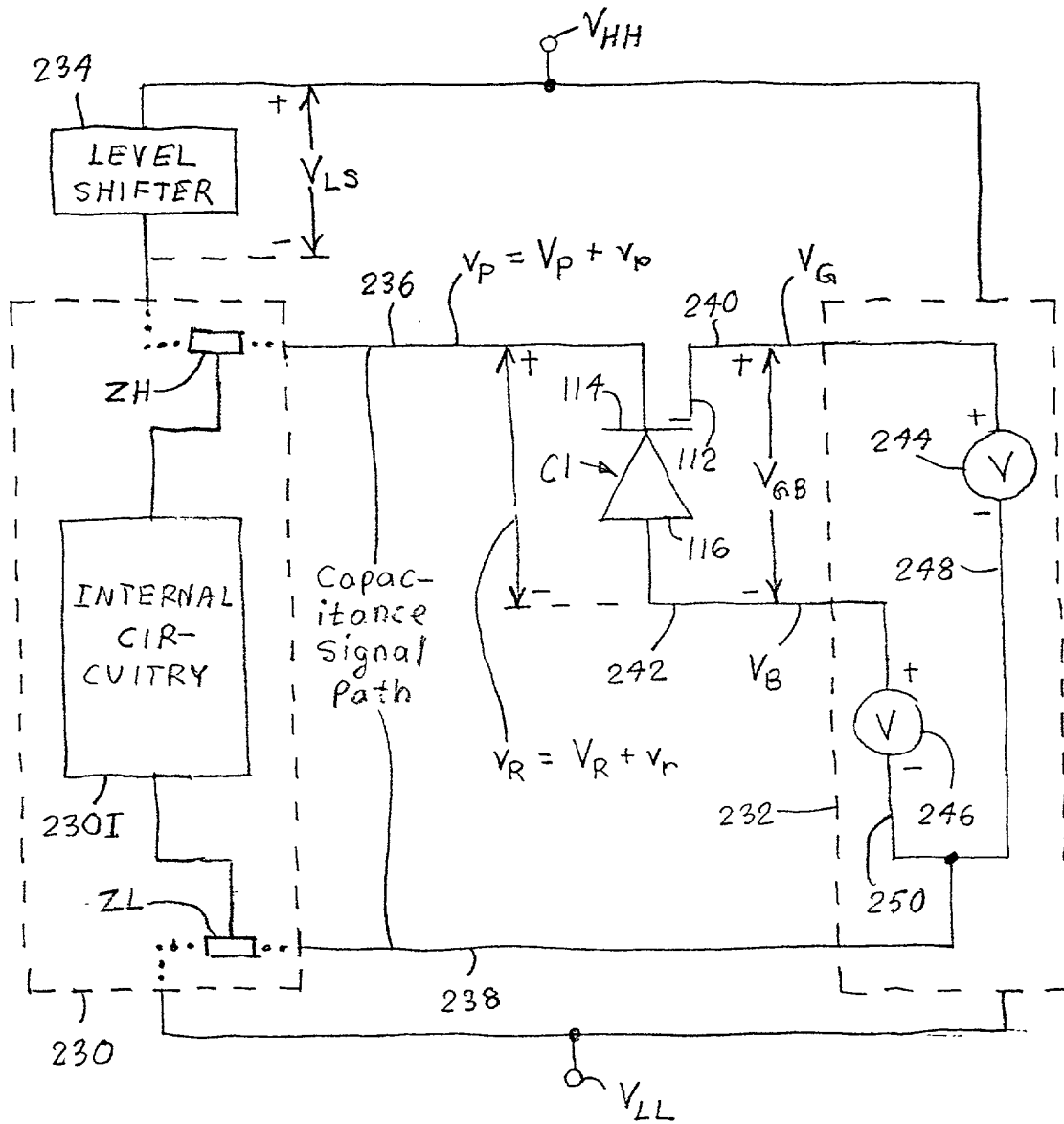
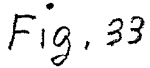


Fig. 32

Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Constantin Buluccea NS-5127 US
Sheet 2 of 26



Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Constantin Bulucea NS-5127 US
Sheet 2 of 26

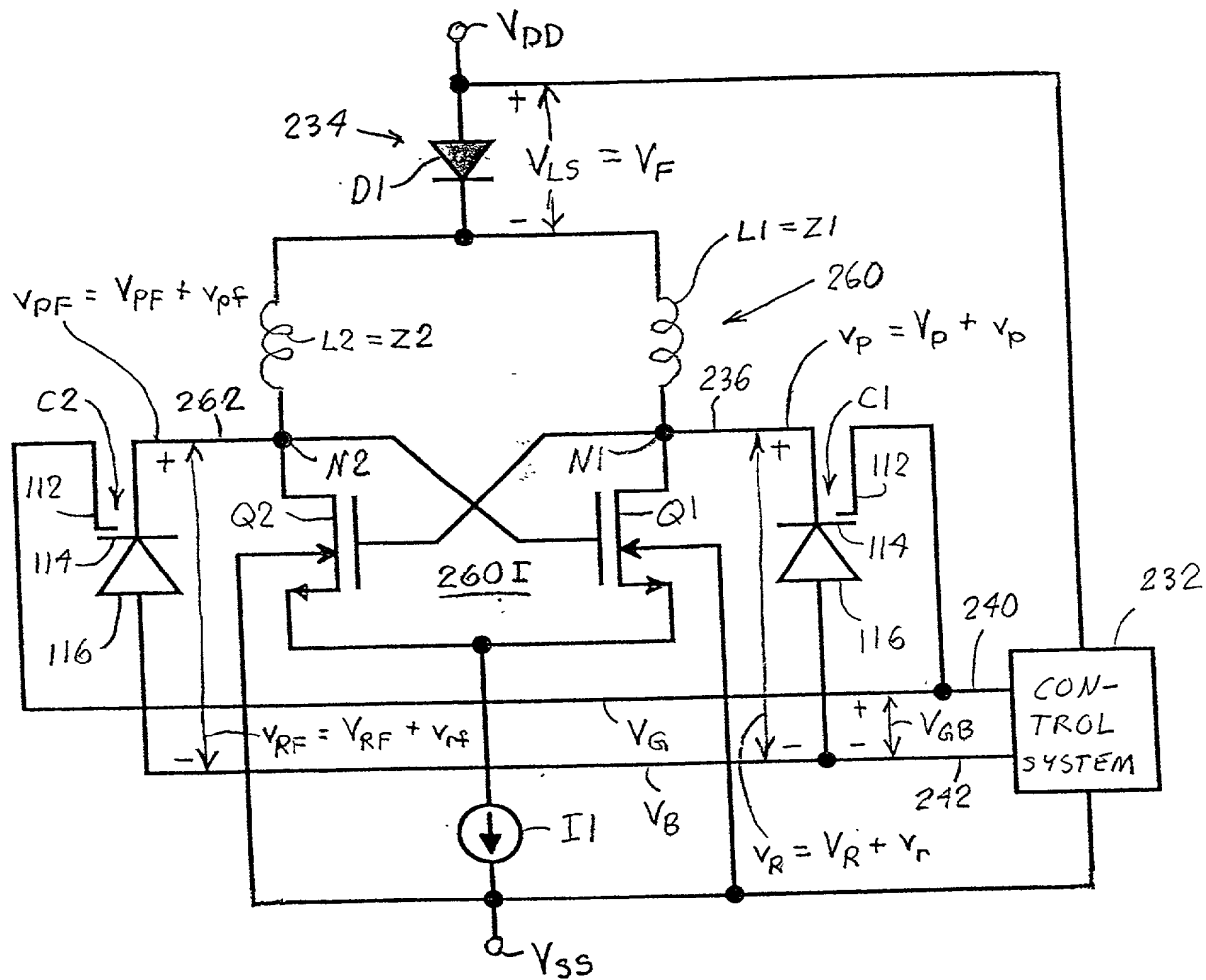


Fig. 34b

Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Constantin Bulucea NS-5127 US
Sheet 1 of 26

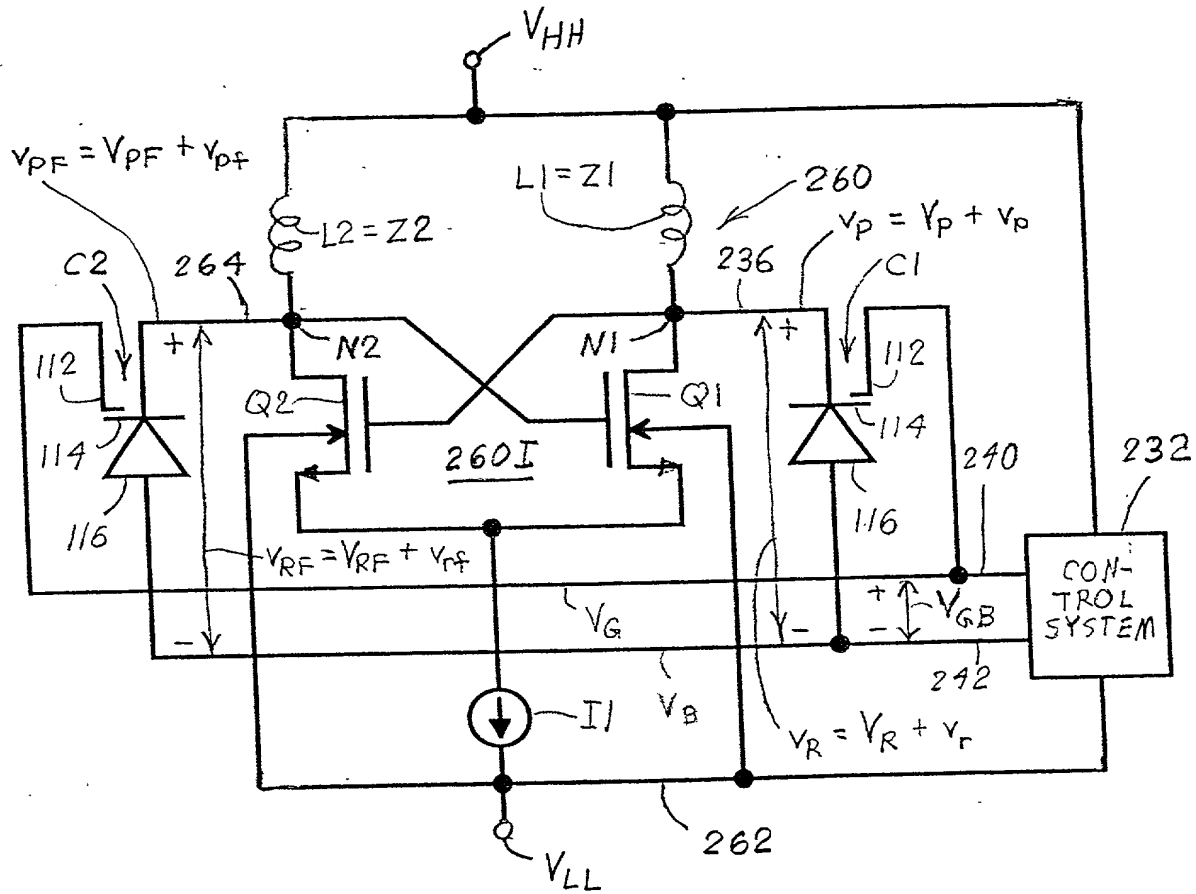


Fig. 34a

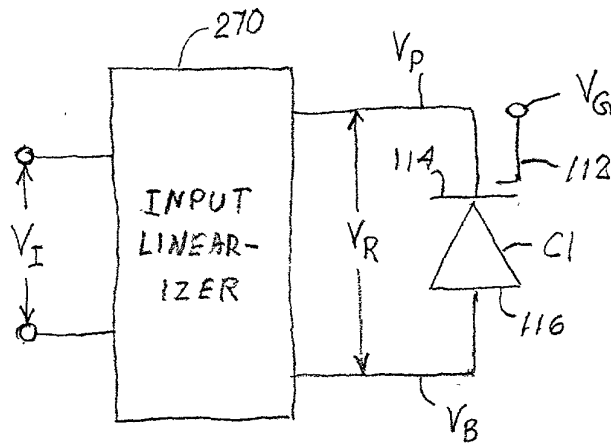


Fig. 35

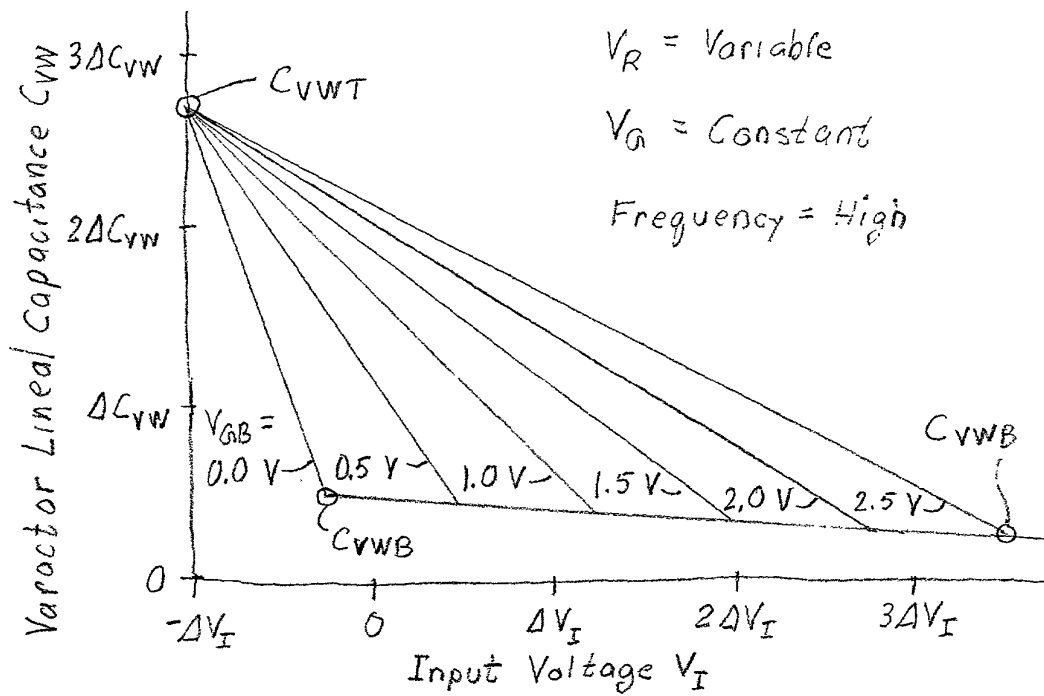
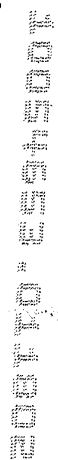


Fig. 36

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